

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Saito, et al.  
Application No.: TBD  
Filed: September 10, 2003  
For: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE  
Expected Group: 2825  
Exp. Examiner: B. Keshavan

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 10, 2003

Sir:

Please amend the above-identified application, prior to examination thereof,  
as listed in the following and as set forth on the following pages:

Amendments to the Specification

Remarks are included following the amendments.